



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KONDO, et al.

Examiner: V. Nguyen

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Group Art Unit: 2858

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Notice of Allow. Date: September 9, 2003

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Title: SEMICONDUCTOR INTEGRATED CIRCUIT WITH NEGATIVE VOLTAGE GENERATION CIRCUIT, TEST METHOD FOR THE SAME, AND RECORDING DEVICE AND COMMUNICATION EQUIPMENT HAVING THE SAME

CERTIFICATE UNDER 37 CFR 1.10:

"Express Mail" mailing label number: EV 322884615 US
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We are transmitting herewith the attached:

- ☒ Transmittal Sheet in duplicate containing Certificate of Mailing
- ☒ Supplemental Information Disclosure Statement, Form 1449, 7 Reference(s), Copy of A Japanese Office Action
- ☒ Check(s) in the amount of \$180 for the IDS filing fee
- ☒ Return postcard

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